

# Test # 2

me: Student id: Section#: Serial#:

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DEPARTMENT OF COMPUTER SCIENCE SUMMER SEMESTER 05  
ITCS241: Assembly Language Programming First Test Date: JULY 25, 2005

QUESTION ONE: Write a complete assembly program that: [6 pts]

- Defines an array YAS consisting of 12 words and initializes them with any HEX signed values.
- Displays all elements of array YAS as words in HEX.
- Copies all words of array YAS to another array FAS in reverse order. (Define array FAS).
- Displays on the next line all words of array FAS as DECIMAL integers. Use space to separate values.

Title Question 1

Include Irvine32.inc

• stack 64  
• data  
Yas words 1h, 2h, 3h, 4h, 5h, 6h, 7h, 8h, 9h, Ah, Bh, Ch  
Fas words 12 dup(?)  
prpt byte "0"

• Code

main proc far

mov si, offset Yas

mov cx, 12

L1: mov eax, 0 ; display Yas

mov ax, [esi]

call writehex

inc si

inc si

Loop L1

call crlf

mov si, offset Yas + 22

mov di, offset Fas

mov cx, 12

.2: mov eax, 0 ; copy

mov ax, [esi]

mov [di], ax

call writedec ; display

inc di

inc di

call writestring ; space

Loop L2

exit

main endp

end

mov eax, 0

mov esi, offset Yas

mov cx, lengthof Yas

L2: mov ax, [esi]

call writehex

inc esi

inc esi

Loop L2

L1: mov ax, [esi]

mov [di], ax

Yas into Fas, reverse

dec si

inc di

Loop L2

mov di, offset fas

mov cx, 12

L2: mov ax, [esi]

QUESTION TWO: Choose the BEST correct answer for each of the following questions (1 ...11) and write its symbol down in the table shown below {11 x 1 = 11 points}

- 1) The .STACK 1024 directive defines:
  - a) A stack of 1024 KByte
  - ☒ b) A stack of 1024 Byte
  - c) A stack of 1024 MByte
  - d) A stack of 1024 bits
  - e) None
- 2) The type of the SOURCE operand used in the instruction: MOV BX, @data is:
  - a) Immediate
  - b) Direct
  - c) Indexed
  - ☒ d) indirect
  - e) None
- 3) The instruction that stores 0 in the memory word pointed by eax register is:
  - a) MOV EAX, 0
  - ☒ b) MOV [EAX], 0
  - c) SUB [EAX], [EAX]
  - d) SUB EAX, EAX
  - e) None
- 4) The register used to store the loop repetition value when using LOOP instruction is:
  - a) ESI
  - b) EBX
  - ☒ c) ECX
  - d) IP
  - e) None
- 5) The program that accepts .obj files as input and produces .exe files as output is:
  - ☒ a) LINK
  - b) MASM
  - c) DEBUG
  - d) Textpad
  - e) None
- 6) The type of the DESTINATION operand used in the instruction: SUB byte ptr [BX], AL is:
  - a) Immediate
  - b) Direct
  - c) Indexed
  - ☒ d) indirect
  - e) None
- 7) The instruction that subtracts the contents of CX register from the word pointed by ebx register is:
  - a) SUB CX, [EBX]
  - b) SUB ebx, CX
  - ☒ c) SUB [ebx], CX
  - d) SUB WORD PTR EBX, CX
  - e) None
- 8) The directive that defines an array OUR consisting of 1000 words all initialized with 0 is:
  - ☒ a) OUR word 1000 dup(0)
  - b) OUR word 1000H dup('0')
  - c) OUR word 1000H dup('0')
  - d) OUR word 1000 dup('0000')
  - e) None
- 9) If the physical address is 7CF0F and the segment value is 78CF, then the offset is:
  - a) 75640
  - b) 78CF0
  - c) 805BF
  - ☒ d) 421F
  - e) None
- 10) The 8-bit value 10001000 represents unsigned decimal value \_\_\_\_\_ and signed decimal value \_\_\_\_\_.
  - a) 136, 120
  - ☒ b) 136, -120
  - c) 136, -136
  - ☒ d) 120, 136
  - e) None
- 11) The instruction that swaps the contents of 2 dwords U1 and U2 stored in memory locations is:
  - a) XCHG U1 U2
  - b) XCHG [U1], U2
  - c) XCHG [U1], [U2]
  - ☒ d) XCHG U2, U1
  - e) None

Question #	1	2	3	4	5	6	7	8	9	10	11
Answer	B	B	B	C	a	d	C	a	d	B	<input checked="" type="radio"/> e

$$786F0 + K = 7CF0F$$

$$786F0 + K = 7CF0F$$

$$K = 7CF0F - 786F0$$

$$\begin{array}{r} 7CF0F \\ 87310 + \\ \hline 421F \end{array}$$

$$\begin{array}{r} 7CF0F \\ 87310 + \\ \hline 421F \end{array}$$

QUESTION THREE: Choose the BEST correct answer for each of the following questions (1 ...4) and write its symbol down in the table shown below [4 x 1 = 4 pts]  
 • Carefully study the following data definitions and instructions then answer the next 5 questions.

RT BYTE 24H, 3CH, 7FH, 9AH  
 PF WORD 5B5AH, 66CCH, 88DFH, 0FFH, 11BAH  
 UT DWORD 8E596D7AH, 56FFBBH, 99BBAACCH, 20807030H  
 MOV BX, WORD PTR RT  
 MOV AX, WORD PTR UT  
 MOV DH, BYTE PTR UT-2  
 MOV CH, TYPE UT  
 MOV CL, LENGTHOF PF

After executing the above instructions,

- The number of bytes occupied by all the above data definitions is :  
 a) 30H b) 30 c) 48H d) 48 e) None
- The register AX will contain:  
 a) 8E59H b) 598EH c) 7A6DH d) 6D7AH e) None
- The register DH will contain:  
 a) 11H b) BAH c) FFH d) 56H e) None
- The register CX will contain:  
 a) 020CH b) 0C02H c) 0405H d) 0C04H e) None

AX = 6D7A  
 CH = 4  
 0A, 04  
 040AH  
 CX = CH CL  
 [04 05]

UT	PF	RT
7A	5A	24
6D	5B	3C
59	CC	7F
8E	66	9A
BB	DF	
FF	82	
56	FF	
0C	0C	
CC	BA	
AA	11	
BB		
99		
30		
70		
80		
20		

DI = BA  
 BX = 5C2

QUESTION FOUR: Study carefully the following code then answer the next 3 questions: [3 x 1 = 3 pts]

```

T9 WORD 44B0H, 3A00H, 2CFCH, 2FAFH, 0F2CH
MOV BX, 1
MOV CX, LENGTHOF T9
MOV AX, 0
L7: ADD AL, BYTE PTR T9[BX]
INC BX
INC BX
LOOP L7
SUB CX, 1
    
```

$BX = 123$   
 $CX = 5$   
 $AX = 0$   
 $AL = 00$

T9	B0	3
	44	1
	00	2
	3A	3
	FC	4
	2C	5
	AF	6
	2F	7
	2E	8
	0F	9

After executing the above code,

1) Register AX will contain  
 a) 00E8 b) 0287

2) Register CX will contain  
 a) 0000 b) FFFF c) 0004 d) Unknown

3) The Parity and Carry flags are equal to  
 a) PO, CY b) PO, NC c) PE, NC d) PE, CY e) None

Parity odd

Carry YES

not carry

Parity even

$BX = 12345678$   
 $CX = 12345$   
 $AX = 0$

$BX = 1$   
 $CX = 548710$   
 $AX = 0$   
 $AL = 00$   
 $BX = 3$   
 $AL = 7E$   
 $BX = 5$   
 $AL = AA$   
 $BX = 7$   
 $AL = D9$   
 $BX = 9$   
 $AL = E8$   
 $CX = FFFF$

$AX = 00E8$

$44H + 3AH + 2CH + 2FAH + 0F2CH$   
 $44H + 3AH + 2CH + 2FAH + 0F2CH$   
 $7E + 2C + 1A + 2F + D9 + 0F + 7E + 4A + 2E + D9$